

CP series CP1H CPU Unit CP1H-X DD - CP1H-Y DD - CP1H-XA D

4 Axis Position Control and Comprehensive Programmable Controller

- The CP1H-X with pulse outputs for 4 axes.
- The CP1H-Y with 1-MHz pulse I/O.
- The CP1H-XA with pulse outputs for 4 axes and built-in analog I/O.

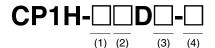


Features

- Pulse output for 4 axes. Advanced power for high-precision positioning control.
- High-speed counters. Differential phases for 4 axes. Easily handles multi-axis control with a single unit.
- Eight interrupt inputs are built in. Faster processing of approximately 500 instructions speeds up the entire system.
- Serial communications. Two ports. Select Option Boards for either RS-232C or RS-485 communications.
- Ethernet Communications. Enabled by using an Option Board. Two ports can be used as an Ethernet port to perform. Ethernet communications between the CP1H and a host computer.
- Built-in Analog I/O. XA CPU Units provide 4 input words and 2 output words.
- USB Peripheral Port. Another standard feature.
- The structured text (ST) language. Makes math operations even easier.
- Can be used for the CP1W series and CJ series Unit. The extendibility of it is preeminently good.
- LCD displays and settings. Enabled using Option Board.

Model Number Structure

■ Model Number Legend (Not all models that can be represented with the model number legend can necessarily be produced.)



1. Class

X: Basic model

XA : Built-in analog I/O terminals Y: Dedicated pulse I/O terminals

2. Number of Built-In number I/O points

40 : 40 I/O points 20 : 20 I/O points 3. Output classification

R: Relay output

T: Transistor Output (sinking)
T1: Transistor Output (sourcing)

4. Power supply A: AC D: DC

Ordering Information

International Standards

- The standards are abbreviated as follows: U: UL, U1: UL (Class I Division 2 Products for Hazardous Locations), C: CSA, UC: cULus, UC1: cULus (Class I Division 2 Products for Hazardous Locations), CU: cUL, N: NK, L: Lloyd, and CE: EC Directives.
- · Contact your OMRON representative for further details and applicable conditions for these standards.

■ CPU Units

		Specificati	ons					
CPU Unit	CPU type	Power supply	Output method	Inputs	Outputs	Model	Standards	
CP1H-X CPU Units	Memory capacity: 20K steps High-speed counters:	AC power supply	Relay output			CP1H-X40DR-A		
	100 kHz, 4 axes Pulse outputs: 100 kHz, 4 axes	DC power	Transistor output (sinking)	24	16	CP1H-X40DT-D		
	(Models with transistor outputs only)	supply	Transistor output (sourcing)			CP1H-X40DT1-D		
CP1H-XA CPU Units	High-speed counters: 100 kHz, 4 axes Pulse outputs: 100 kHz, 4 axes	AC power supply	Relay output			CP1H-XA40DR-A		
		Pulse outputs: 100 kHz, 4 axes	DC power	Transistor output (sinking)	24	16	CP1H-XA40DT-D	UC1, N, L, CE
Ana		supply	Transistor output (sourcing)			CP1H-XA40DT1-D		
CP1H-Y CPU Units	Memory capacity: 20K steps High-speed counters: 1 MHz, 2 axes 100 kHz, 2 axes Pulse outputs:1 MHz, 2 axes 100 kHz, 2 axes	DC power supply	Transistor output (sinking)	12 + line-driver input, 2 axes	8 + line-driver output, 2 axes	CP1H-Y20DT-D		

Note: 1. CP1H PLCs are supported by CX-Programmer version 6.1 or higher.

2. Purchase a separately sold Option Unit if you will use RS-232C, RS-422A/485, Ethernet, or LCD.

■ Options for CPU Units

Name		Specifications	Model	Standards
RS-232C Option Board			CP1W-CIF01	UC1, N,
RS-422A/485 Option Board		Can be mounted in either CPU Unit Option Board slot 1 or 2.	CP1W-CIF11	L, CE
RS-422A/485 (Isolated-type) Option Board			CP1W-CIF12	UC1, N, L, CE
Ethernet Option Board		Can be mounted in either CPU Unit Option Board slot 1 or 2. *	CP1W-CIF41	UC1, N, L, CE
LCD Option Board		Can be mounted only in the CPU Unit Option Board slot 1.	CP1W-DAM01	UC1, L, N, CE
Memory Cassette		Can be used for backing up programs or auto-booting.	CP1W-ME05M	UC1, N, L, CE

^{*} When using CP1W-CIF41 Ver.1.0, one Ethernet port can be added.

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■ Programming Devices

	Specifications				
Name		Number of licenses	Media	Model	Standards
FA Integrated Tool Package CX-One Lite Version 4.□	CX-One Lite is a subset of the complete CX-One package that provides only the Support Software required for micro PLC applications. CX-One Lite runs on the following OS. OS: Windows XP (Service Pack 3 or higher, 32-bit version) / Windows Vista (32-bit/64-bit version) / Windows 7 (32-bit/64-bit version) / Windows 8.1 (32-bit/64-bit version)	1 license	CD	CXONE-LT01C-V4	
	CX-One Lite Ver. 4. ☐ includes Micro PLC Edition CX-Programmer Ver. 9. ☐.				
FA Integrated Tool Package CX-One Ver. 4.□	CX-One is a package that integrates the Support Software for OMRON PLCs and components. CX-One runs on the following OS. OS: Windows XP (Service Pack 3 or higher, 32-bit version) / Windows Vista (32-bit/64-bit version) / Windows 8 (32-bit/64-bit version) / Windows 8.1 (32-bit/64-bit version)	1 license (See note 3.)	DVD (See note 4.)	CXONE-AL01D-V4	
	CX-One Ver. 4.□ includes CX-Programmer Ver. 9.□.				
Programming Device	Connects Personal Computers, D-Sub 9-pin (Length: 2.0 m)	For anti-static	connectors	XW2Z-200S-CV	
Connecting Cable for CP1W-CIF01 RS-232C	Connects Personal Computers, D-Sub 9-pin (Length: 5.0 m)	- FOI anti-Static	Connectors	XW2Z-500S-CV	
Option Board	Connects Personal Computers, D-Sub 9-pin (Length: 2.0 m)			XW2Z-200S-V	
(See note 5.)	Connects Personal Computers, D-Sub 9-pin (Length: 5.0 m)			XW2Z-500S-V	
USB-Serial Conversion Cable (See note 5.)	USB-RS-232C Conversion Cable (Length: 0.5 m) and PC driver included. Complies with USB Specification 2.0 On personal computer side: USB (A plug connector, male) On PLC side: RS-232C (D-sub 9-pin, male) Driver: Supported by Windows 98, Me, 2000, XP(32bit), Vista(and 8/32bit/64bit)	,	CS1W-CIF31	N	

Note: 1. CP1H PLCs are supported by CX-Programmer version 6.1 or higher.

Update The CX-Programmer version automatically from the website using CX-Programmer version 7.0 (included with CX-One version 2.0).

- 2. The CX-One and CX-One Lite cannot be simultaneously installed on the same computer.
- 3. Multi licenses are available for the CX-One (3, 10, 30 or 50 licenses).
- **4.** The CX-One is also available on CD (CXONE-AL□□C-V4).
- 5. Cannot be used with a peripheral USB port.

To connect to a Personal Computers via a peripheral USB port, use commercially-available USB cable (A or B type, male).

The following tables lists the Support Software that can be installed from CX-One

Support Software in CX-One		CX-One Lite Ver.4.□	CX-One Ver.4.□	Support Software in CX-One		CX-One Lite Ver.4.□	CX-One Ver.4.□
Micro PLC Edition CX-Programmer	Ver.9.□	Yes	No	CX-Drive	Ver.1.□	Yes	Yes
CX-Programmer	Ver.9.□	No	Yes	CX-Process Tool	Ver.5.□	No	Yes
CX-Integrator	Ver.2.□	Yes	Yes	Faceplate Auto-Builder for NS	Ver.3.□	No	Yes
Switch Box Utility	Ver.1.□	Yes	Yes	CX-Designer	Ver.3.□	Yes	Yes
CX-Protocol	Ver.1.□	No	Yes	NV-Designer	Ver.1.□	Yes	Yes
CX-Simulator	Ver.1.□	Yes	Yes	CX-Thermo	Ver.4.□	Yes	Yes
CX-Position	Ver.2.□	No	Yes	CX-ConfiguratorFDT	Ver.1.□	Yes	Yes
CX-Motion-NCF	Ver.1.□	No	Yes	CX-FLnet	Ver.1.□	No	Yes
CX-Motion-MCH	Ver.2.□	No	Yes	Network Configurator	Ver.3.□	Yes	Yes
CX-Motion	Ver.2.□	No	Yes	CX-Server	Ver.4.□	Yes	Yes

Note: For details, refer to the CX-One Catalog (Cat. No: R134).

■ Expansion Units

Name)	Output method	Inputs	Outputs	Model	Standards	
		Relay			CP1W-40EDR		
		Transistor (sinking)	24	16	CP1W-40EDT	N, L, CE	
	a a a a a a a a a a a a a a a a a a a	Transistor (sourcing)			CP1W-40EDT1		
		Relay			CP1W-32ER		
		Transistor (sinking)		32	CP1W-32ET	N, L, CE	
		Transistor (sourcing)			CP1W-32ET1		
	ā	Relay			CP1W-20EDR1		
		Transistor (sinking)	12	8	CP1W-20EDT	U, C, N, L, CE	
Expansion I/O Units	a december :	Transistor (sourcing)			CP1W-20EDT1		
	_a	Relay			CP1W-16ER		
	Entire De la Constitución de la	Transistor (sinking)		16	CP1W-16ET	N, L, CE	
	Personne :	Transistor (sourcing)			CP1W-16ET1		
			8		CP1W-8ED		
		Relay		8	CP1W-8ER		
		Transistor (sinking)		8	CP1W-8ET	U, C, N, L, CE	
		Transistor (sourcing)			CP1W-8ET1	1	
Analog Input Unit		Analog (resolution: 1/6000)	4		CP1W-AD041	LICA N. L. CE	
Analog Output Unit	ā	Analog (resolution: 1/6000)		4	CP1W-DA041	— UC1, N, L, CE	
Analog Output Omit	4 <u> </u>	Analog (resolution: 1/0000)		2	CP1W-DA021	UC1, CE	
Analog I/O Unit		Analog (resolution: 1/6000)	2	1	CP1W-MAD11	U, C, N, L, CE	
CompoBus/S I/O Link Unit			8 (I/O link input bits)	8 (I/O link input bits)	CP1W-SRT21		
	_	2 thermocouple inputs			CP1W-TS001	U, C, N, L, CE	
Temperature Sensor		4 thermocouple inputs			CP1W-TS002		
Unit		2 platinum resistance thermon	neter inputs		CP1W-TS101		
	FERRENCE	4 platinum resistance thermon	neter inputs		CP1W-TS102		

■ I/O Connecting Cable

Name Specifications		Model	Standards
I/O Connecting Cable	80 cm (for CP1W/CPM1A Expansion Units)	CP1W-CN811	UC1, N, L, CE

Note: An I/O Connecting Cable (approx. 6 cm) for horizontal connection is provided with CP1W/CPM1A Expansion Units.

■ Optional Products, Maintenance Products and DIN Track Accessories

Name Specifications		Specifications	Model	Standards
Е	Battery Set For CP1H CPU Units (Use batteries within two years of manufacture.)		CJ1W-BAT01	CE
		Length: 0.5 m; Height: 7.3 mm	PFP-50N	
C	IN Track	Length: 1 m; Height: 7.3 mm	PFP-100N	
		Length: 1 m; Height: 16 mm	PFP-100N2	
	End Plate	There are 2 stoppers provided with a CJ Unit Adapter as standard accessories to secure the Units on the DIN Track.	PFP-M	

■ CJ-series Special I/O Units and CPU Bus Units

Category	Name	Specifications	Model	Standards	
CP1H CPU Unit options	CJ Unit Adapter	Adapter for connecting CJ-series Special I/O Units and CPU Bus Units (includes CJ-series End Cover and 2 End Plates)	CP1W-EXT01	UC1, N, L, CE	
		4 inputs (1 to 5 V (1/10,000), 0 to 10 V (1/20,000), -5 to 5 V (1/20,000), -10 to 10 V (1/40,000), and 4 to 20 mA (1/10,000)) Conversion Period: 20 μs/1 point, 25 μs/2 points, 30 μs/3 points, 35 μs/4 points		UC1, CE	
	Analog Input Units	8 inputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/8,000, Conversion speed: 250 µs/input max. (Can be set to 1/4,000 resolution and 1 ms/input.)	CJ1W-AD081-V1	UC1, N, L,	
		4 inputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/8,000, Conversion speed: 250 µs/input max. (Can be set to 1/4,000 resolution and 1 ms/input.)	CJ1W-AD041-V1	CE	
		4 outputs (1 to 5 V (1/10,000), 0 to 10 V (1/20,000), and -10 to 10 V (1/40,000) Conversion Period: 20 μs/1 point, 25 μs/2 points, 30 μs/3 points, 35 μs/4 points	CJ1W-DA042V	UC1, CE	
		8 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V) Resolution: 1/4,000; Conversion speed: 1 ms/output max. (Can be set to 1/8000, 250 µs/output.)	CJ1W-DA08V	UC1, N, L, CE	
	Analog Output Units	8 outputs (4 to 20 mA) Resolution: 1/4,000; Conversion speed: 1 ms/output max. (Can be set to 1/8,000, 250 µs/ output.)	CJ1W-DA08C	UC1, N, CE	
		4 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/4,000, Conversion speed: 1ms/point max.	CJ1W-DA041		
		2 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/4,000, Conversion speed: 1ms/point max.	CJ1W-DA021	UC1, N, L,	
	Analog I/O Unit	4 inputs, 2 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/4000; Conversion speed: 1 ms/point max. (Can be set to 1/8,000, 500 µs/point.)	CJ1W-MAD42		
	Process Input Units	4 fully universal inputs: Pt100 (3-wire), JPt100 (3-wire), Pt1000 (3-wire), Pt100 (4 wire), K, J, T, E, L, U, N, R, S, B, WRe5-26, PLII, 4 to 20 mA, 0 to 20 mA, 1 to 5 V, 0 to 1.25 V, 0 to 5 V, 0 to 10 V, ±100-mV selectable range, -1.25 to 1.25 V, -5 to 5 V, -10 to 10 V, ±10-V selectable range Potentiometer resolution/conversion speed: 1/256,000 (conversion cycle: 60 ms/4 points), 1/64,000 (conversion cycle: 10 ms/4 points), 1/16,000 (conversion cycle: 5 ms/4 points)	CJ1W-PH41U *	UC1, CE	
		4 fully universal inputs: Pt100, JPt100, Pt1000, K, J, T, L, R, S, B, 4 to 20 mA, 0 to 20 mA, 1 to 5 V, 0 to 5 V, 0 to 10 V Conversion speed: 250 ms/4 points	CJ1W-AD04U	UC1, L, CE	
		4 inputs, B, J, K, L, R, S, T; Conversion speed: 250 ms/4 inputs	CJ1W-PTS51		
CJ1 Special /O Units		4 inputs, Pt100 Ω (JIS, IEC), JPt100 Ω , Conversion speed: 250 ms/4 inputs	CJ1W-PTS52		
		2 inputs, B, E, J, K, L, N, R, S, T, U, W, Re5-26, PL ±100 mV, Resolution: 1/64,000; Conversion speed: 10 ms/2 inputs	CJ1W-PTS15	UC1, CE	
		2 inputs, 0 to 1.25 V, -1.25 to 1.25 V, 0 to 5 V, 1 to 5 V, -5 to 5 V, 0 to 10 V, -10 to 10 V, ±10-V selectable range, 0 to 20 mA, 4 to 20 mA	CJ1W-PDC15		
		4 loops, thermocouple input, NPN output	CJ1W-TC001		
		4 loops, thermocouple input, PNP output 2 loops, thermocouple input, NPN output, heater burnout detection function	CJ1W-TC002	-	
			CJ1W-TC003		
	Temperature Control	2 loops, thermocouple input, PNP output, heater burnout detection function 4 loops, platinum resistance thermometer input, NPN output	CJ1W-TC004 CJ1W-TC101	UC1, N, L,	
	Units	4 loops, platinum resistance thermometer input, NPN output 4 loops, platinum resistance thermometer input, PNP output	CJ1W-TC101	CE CE	
		2 loops, platinum resistance thermometer input, NPN output, heater burnout detection function	CJ1W-TC102 CJ1W-TC103		
		2 loops, platinum resistance thermometer input, PNP output, heater burnout detection function	CJ1W-TC104		
	High-speed Counter Unit	2 inputs, max. input frequency: 500 kpps	CJ1W-CT021	UC1, N, L, CE	
		Pulse train, open collector output, 1 axis	CJ1W-NC113		
		Pulse train, open collector output, 2 axes	CJ1W-NC213		
	Position Control Units	Pulse train, open collector output, 4 axes	CJ1W-NC413		
	r osmon control onls	Pulse train, line driver output, 1 axis	CJ1W-NC133	UC1, CE	
		Pulse train, line driver output, 2 axes	CJ1W-NC233		
		Pulse train, line driver output, 4 axes	CJ1W-NC433	1	
	Space Unit		CJ1W-SP001	1	
		For V680 Series, 1 R/W Head	CJ1W-V680C11		
		For V680 Series, 2 R/W Heads	CJ1W-V680C12	1	
	ID Sensor Units	For V600 Series, 1 R/W Head	CJ1W-V680C12 UC, Cl		
		For V600 Series, 2 R/W Heads	CJ1W-V600C12	1	
	CompoNet Master Unit	Word slaves: 2,048 points, Bit slaves: 512 points	CJ1W-CRM21	U, U1, N, L CE	

^{*} If a CJ1W-PH41U is used, do not use a CP1H CPU Unit with relay contact outputs or Expansion Units with relay contact outputs.

Note: Refer to the CJ1 catalog (Cat. No. P052) for information on the CJ1 Special I/O Units.

Category	Name	Specifications		Model	Standards	
	Controller Link Units	Wired (shielded twisted-pair cable)		CJ1W-CLK23	UC1, N, L, CE	
		1 RS-232C port and 1 RS-422A/485 port		CJ1W-SCU42		
		2 RS-232C ports		CJ1W-SCU22	UC1, N, L, CE	
	Serial Communications	2 RS-422A/485 ports		CJ1W-SCU32		
	Units	1 RS-232C port and 1 RS-422A/485 port		CJ1W-SCU41-V1		
		2 RS-232C ports	CJ1W-SCU21-V1	UC1, N, L, CE		
		2 RS-422A/485 ports	CJ1W-SCU31-V1			
CJ1 CPU Bus Units	EtherNet/IP Unit	Shielded twisted-pair cable (STP), category 5 or Tag data links and message communications sup	CJ1W-EIP21			
bus Units	Ethernet Unit	100Base-TX	CJ1W-ETN21			
	DeviceNet Unit	Functions as master and/or slave; allows control master	CJ1W-DRM21			
		Control or annual description MEQUATROUNIX II	2 axes	CJ1W-NC271		
	MECHATROLINK-II	Control commands sent using MECHATROLINK-II synchronized communications	4 axes	CJ1W-NC471	UC1, CE	
	Position Control Unit	16 axes max., direct operation from ladder diagram, control modes: position/ speed/torque	16 axes	CJ1W-NCF71		
		diagram, control modes. position/ speed/torque	16 axes	CJ1W-NCF71-MA		
	FI-net Unit	100Base-TX	•	CJ1W-FLN22		
	SPU	High-speed Data Storage Unit		CJ1W-SPU01-V2		

Note: Refer to the *CJ1 catalog* (Cat. No. P052) for information on the CJ1 CPU Bus Units.

■ Industrial Switching Hubs

		Specification	pecifications			Current		
Product name	Appearance	Functions	No. of ports	Failure detection	Accesories	consumption (A)	Model	Standards
Industrial		Quality of Service (QoS): EtherNet/IP control data priority Failure detection:	3	No	Power supply connector	0.08	W4S1-03B	UC, CE
Switching Hubs	_	Broadcast storm and LSI error	5	No		0.12	W4S1-05B	
		detection 10/100BASE-TX, Auto-Negotiation	5	Yes	Power supply connectorConnector for informing error	0.12	W4S1-05C	CE

General Specifications

Туре	AC power supply models	DC power supply models			
Item Model	CP1H-□□□-A	CP1H-□□□-D			
Power supply	100 to 240 VAC 50/60 Hz	24 VDC			
Operating voltage range	85 264 VAC	20.4 to 26.4 VDC (with 4 or more Expansion Units and Expansion I/O Units: 21.6 to 26.4 VDC)			
Power consumption	100 VA max. (CP1H-□□□-A)(page 25)	50 W max. (CP1H-□□□-D)(page 25)			
		30 A max. (for cold start at room temperature) 20 ms max.			
External power supply	300 mA at 24 VDC	None			
Insulation resistance	$20~\text{M}\Omega$ min. (at 500 VDC) between the external AC terminals and GR terminals	No insulation between primary and secondary for DC power supply			
Dielectric strength	2,300 VAC at 50/60 Hz for 1 min between the external AC and GR terminals, leakage current: 5 mA max.	No insulation between primary and secondary for DC power supply			
Noise immunity	Conforms to IEC 61000-4-4. 2 kV (power supply line)				
Vibration resistance	Conforms to JIS C60068-2-6. 10 to 57 Hz, 0.075-mm amplitude, 80 minutes each. Sweep time: 8 minutes × 10 sweeps = total times.				
Shock resistance	Conforms to JIS C60068-2-27. 147 m/s² three times each in X, Y	/, and Z directions			
Ambient operating temperature	0 to 55°C				
Ambient humidity	10% to 90% (with no condensation)				
Ambient operating environment	No corrosive gas				
Ambient storage temperature	-20 to 75°C (Excluding battery.)				
Power holding time	10 ms min.	2 ms min.			

Note: The above values are for a cold start at room temperature for an AC power supply, and for a cold start for a DC power supply.

- A thermistor (with low-temperature current suppression characteristics) is used in the inrush current control circuitry for the AC power supply. The thermistor will not be sufficiently cooled if the ambient temperature is high or if a hot start is performed when the power supply has been OFF for only a short time. In those cases the inrush current values may be higher (as much as two times higher) than those shown above. Always allow for this when selecting fuses and breakers for external circuits.
- A capacitor charge-type delay circuit is used in the inrush current control circuitry for the DC power supply. The capacitor will not be charged if a hot start is performed when the power supply has been OFF for only a short time, so in those cases the inrush current values may be higher (as much as two times higher) than those shown above.

Performance Specifications

	Tyrna	CP1H-XA CPU Units	CP1H-X CPU Units	CP1H-Y CPU Units		
Item	Type Models	CP1H-XA CPU UNITS	CP1H-X CP0 Units	CP1H-Y CP0 Units		
Control met		Stored program method	CF III-X	CFIII-1		
I/O control n		Cyclic scan with immediate refresh	ing			
	Program language Ladder diagram					
Maximum number of function block definitions: 128 Maximum number of instances: 256						
Function blo	ocks	Languages usable in function block				
Instruction I	ength	1 to 7 steps per instruction				
Instructions		Approx. 500 (function codes: 3 digit	ts)			
Instruction e	execution time	Basic instructions: 0.10 μs min. Spe	*			
Common pr	ocessing time	0.7 ms				
Program cap	pacity	20K steps				
Number of t	asks	288 (32 cyclic tasks and 256 interru	ıpt tasks)			
	Scheduled	1 (interrupt task No. 2, fixed)				
	interrupt tasks	1 (interrupt task No. 2, fixed)				
	Input interrupt	8 (interrupt task No. 140 to 147, fixe	ed)	6 (interrupt task No. 140 to 145, fixed)		
	tasks	(Interrupt tasks can also be specifie	ed and executed for high-speed cou	nter interrupts.)		
Maximum su	ubroutine number	256				
Maximum ju	•	256				
	Input bits	272bits (17 words) : CIO 0.00 to 16				
	Output bits	272bits (17 words) : CIO 100.00 to	116.16			
I/O areas	Built-in Analog	CIO 200 to CIO 203				
(See note.)	Built-in Analog	CIO 210 to CIO 211				
	Outputs Serial PLC Link	1.440 bits (90 words): CIO 3100.00	to CIO 3189.15 (CIO 3100 to CIO	3189)		
	Area					
Work bits		8,192 bits (512 words): W0.00 to W511.15 (W0 to W511) CIO Area: 37,504 bits (2,344 words): CIO 3800.00 to CIO 6143.15 (CIO 3800 to CIO 6143)				
TR Area		16 bits: TR0 to TR15				
Holding Are	a	8,192 bits (512 words): H0.00 to H511.15 (H0 to H511)				
AR Area		Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A447.15 (A0 to A447)				
		Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to A959)				
Timers		4,096 bits: T0 to T4095 4,096 bits: C0 to C4095				
Counters DM Area		32 Kwords: D0 to D32767				
Data Registe	or Aroa	32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15				
Index Regist		16 registers (32 bits): IR0 to IR15				
Task Flag A		32 flags (32 bits): TK0000 to TK0031				
Trace Memo		4,000 words (500 samples for the trace data maximum of 31 bits and 6 words.)				
Memory Cas	<u>.</u>	A special Memory Cassette (CP1W-ME05M) can be mounted. Note: Can be used for program backups and auto-booting.				
		Supported. Accuracy (monthly devi		nt temperature: 55°C)		
Clock functi	on		perature: 25° C), -2.5 min to $+1.5$ m			
		-	.1): For connecting Support Softwar			
Communica	tions functions	A maximum of two Serial Commun	ications Option Boards can be mou	nted.		
Communica	tions functions	A maximum of two Ethernet Option Boards can be mounted. When using CP1W-CIF41 Ver.1.0, one Ethernet Option Board can be mounted.				
Memory bac	kup	Flash memory: User programs, parameters (such as the PLC Setup), comment data, and the entire DM Area can be saved to flash memory as initial values.				
			DM Area, and counter values (flags			
Battery serv	ice life	5 years at 25°C. (Use the replacem	ent battery within two years of man			
Built-in input terminals		40 (24 inputs, 16 outputs)		20 (12 inputs, 8 outputs) Line-driver inputs: Two axes for phases A, B, and Z Line-driver outputs: Two axes for CW and CCW		
Number of connectable Expansion (I/O) Units		CP Expansion I/O Units: 7 max.; Co	J-series Special I/O Units or CPU B	us Units: 2 max.		
Max. number of I/O points		320 (40 built in + 40 per Expansion	(I/O) Unit × 7 Units)	300 (20 built in + 40 per Expansion (I/O) Unit × 7 Units)		
Interrupt inp	outs	8 inputs (Shared by the external int the quick-response inputs.)	errupt inputs (counter mode) and	6 inputs (Shared by the external interrupt inputs (counter mode) and the quick-response inputs.)		
Interrupt inp	out counter mode	8 inputs (Response frequency: 5 kl 16 bits	Hz max. for all interrupt inputs),	6 inputs (Response frequency: 5 kHz max. for all interrupt inputs), 16 bits		
Out 1		Up or down counters	\	Up or down counters		
Quick-respo		8 points (Min. input pulse width: 50	µ5 max.)	6 points (Min. input pulse width: 50 μs max.)		
Scheduled in	nterrupts	1				

	Туре	CP1H-XA CPU Units	CP1H-X CPU Units	CP1H-Y CPU Units
Item	Models	CP1H-XA	CP1H-X	CP1H-Y□□-□
High-speed cour	nters	100 kHz Value range: 32 bits, Line	direction, up/down, increment),	2 inputs: Differential phases (4x), 500 kHz or Single-phase, 1 MHz and 2 inputs: Differential phases (4x), 50 kHz or Single-phase (pulse plus direction, up/down, increment), 100 kHz Value range: 32 bits, Linear mode or ring mode Interrupts: Target value comparison or range comparison
Pulse outputs (models with transistor out- puts only)	Pulse out- puts	Trapezoidal or S-curve acceleration (Duty ratio: 50% fixed) 4 outputs, 1 Hz to 100 kHz (CCW/4)		Trapezoidal or S-curve acceleration and deceleration (Duty ratio: 50% fixed) 2 outputs, 1 Hz to 1 MHz (CCW/CW or pulse plus direction) 2 outputs, 1 Hz to 100 kHz (CCW/CW or pulse plus direction)
	PWM out- puts	Duty ratio: 0.0% to 100.0% (Unit: 0 2 outputs, 0.1 to 6553.5 Hz (Accura	,	
Built-in analog I/	O terminals	4 analog inputs and 2 analog outputs	None	
Analog control		1 (Setting range: 0 to 255)		
External analog	input	1 input (Resolution: 1/256, Input ra	inge: 0 to 10 V), not isolated	

Note: The memory areas for CJ-series Special I/O Units and CPU Bus Units are allocated at the same as for the CJ-series. For details, refer to the CJ Series catalog (Cat. No. P052).

Built-in Inputs / Built-in Outputs

■ Terminal Block Arrangement

● CP1H-XA and X CPU Units with AC Power Supply

		- 1	CIO)										10	CIO 1	1										
Γ	L1 🔷 L	2/N	CO	М	01	00	3	05	0	7	09	,	11	1	01	Т	03	0	5	07	, [09	T	11	1	(Input
ľ	• 🛊		\supset	00	()2	04	1	06	01	В	10		00)	02		04	06	ŝ	08	В	10	0	•	terminals)
-																										

	+	0	00		01	()2	0	3	0	4	0	6	(00	0	1	0)3	0	14	0	6	•	(Output
•		_	-	M	CC	M	COI	м	CO	М	05	5	0		CC		02	2	CC	M	05	5	0	7	terminals)
			CIO	100											CIO	101									

● CP1H-XA and X CPU Units with DC Power supply

			ICIO	0										JC	IO 1											
+	+⊢	-	CC	MC	01	0	3	05	0.	7	09		- 11		01	0:	3	05	П	07		09)	11	1	(Input
•	NC	(∌	0	0	02	0	4	06	0	18	10	0	00		02	0	4	0	6	0	8	1	0	•	terminals)

N	IC	0	00)1	0	12	0	03	0	14	0	16	(10	0	1	(03		14	0	6	•	(Output
•	N		СО	M	СО	М	co	М	CO	М	05	5	07		CO		02	2	CO	М	05	5	0	7	terminals)

■ Built-in Input Area

● CP1H-XA and X CPU Units

PLC Se	etup		Input operati	on	High-speed counter operation	Pulse output origin search function set to be used.
		Normal inputs	Interrupt inputs	Quick-response inputs	High-speed counters	Origin search
CIO 0	00	Normal input 0	Interrupt input 0	Quick-response input 0		Pulse 0: Origin input signal
	01	Normal input 1	Interrupt input 1	Quick-response input 1	High-speed counter 2 (phase-Z/reset)	Pulse 0: Origin proximity input signal
	02	Normal input 2	Interrupt input 2	Quick-response input 2	High-speed counter 1 (phase-Z/reset)	Pulse output 1: Origin input signal
	03	Normal input 3	Interrupt input 3	Quick-response input 3	High-speed counter 0 (phase-Z/reset)	Pulse output 1: Origin proximity input signal
	04	Normal input 4			High-speed counter 2 (phase-A, increment, or count input)	
	05	Normal input 5			High-speed counter 2 (phase-B, decrement, or direction input)	
	06	Normal input 6			High-speed counter 1 (phase-A, increment, or count input)	
	07	Normal input 7			High-speed counter 1 (phase-B, decrement, or direction input)	
	08	Normal input 8			High-speed counter 0 (phase-A, increment, or count input)	
	09	Normal input 9			High-speed counter 0 (phase-B, decrement, or direction input)	
	10	Normal input 10			High-speed counter 3 (phase-A, increment, or count input)	
	11	Normal input 11			High-speed counter 3 (phase-B, decrement, or direction input)	
CIO 1	00	Normal input 12	Interrupt input 4	Quick-response input 4	High-speed counter 3 (phase-Z/reset)	Pulse output 2: Origin input signal
	01	Normal input 13	Interrupt input 5	Quick-response input 5		Pulse output 2: Origin proximity input signal
	02	Normal input 14	Interrupt input 6	Quick-response input 6		Pulse output 3: Origin input signal
	03	Normal input 15	Interrupt input 7	Quick-response input 7		Pulse output 3: Origin proximity input signal
	04	Normal input 16				
	05	Normal input 17				
	06	Normal input 18				
	07	Normal input 19				
	80	Normal input 20				
	09	Normal input 21				
	10	Normal input 22				
	11	Normal input 23				

■ Built-in Output Area

● CP1H-XA and CP1H-X CPU Units

	ruc- ions	When the instructions to the right are not executed		output instruction , or ORG) is executed	When the origin search function is set to be used in the PLC Setup, and an origin search is executed by the ORG instruction	When the PWM instruction is executed
PLC S	cotun	Normal outputs		Fixed duty ratio p	ulse outputs	Variable duty ratio pulse output
FLC 3	etup	Normai outputs	CW/CCW	Pulse plus direction	When the origin search function is used	PWM output
CIO	00	Normal output 0	Pulse output 0 (CW)	Pulse output 0 (pulse)		
100	01	Normal output 1	Pulse output 0 (CCW)	Pulse output 1 (pulse)		
	02	Normal output 2	Pulse output 1 (CW)	Pulse output 0 (direction)		
	03	Normal output 3	Pulse output 1 (CCW)	Pulse output 1 (direction)		
	04	Normal output 4	Pulse output 2 (CW)	Pulse output 2 (pulse)		
	05	Normal output 5	Pulse output 2 (CCW)	Pulse output 2 (direction)		
	06	Normal output 6	Pulse output 3 (CW)	Pulse output 3 (pulse)		
	07	Normal output 7	Pulse output 3 (CCW)	Pulse output 3 (direction)		
CIO	00	Normal output 8				PWM output 0
101	01	Normal output 9				PWM output 1
	02	Normal output 10			Origin search 0 (Error counter reset output)	
	03	Normal output 11			Origin search 1 (Error counter reset output)	
	04	Normal output 12			Origin search 2 (Error counter reset output)	
	05	Normal output 13			Origin search 3 (Error counter reset output)	
CIO	06	Normal output 14				
101	07	Normal output 15				

■ Terminal Block Arrangement

● CP1H-Y CPU Units



Γ	NC	CV	V0+	CC'	W0+	CV	V1+	CC	W1	N	С	NC	Т	04	(15	0	7	0	0	0	2	•	(0
•	N	С	CW	/0-	CCV	٧٥-	CW	/1-	CCV	V1-	+	4-	-	CC	M	06	П	CC	М	01		00	3	(Output terminals)
			PUL	SE							CIO.	100		,				CIO	101					
				Llr	ne-d	rive	er oi	utoi	ıts		(Se	ee n	ote.)											

Note: Supply 24 VDC to the bottom 24 VDC input terminals when using bits 04 to 07 of output word CIO 100.

■ Built-in Input Area

● CP1H-Y CPU Units

PLC S	Setup		Input operation	setting	High-speed counter operation setting	Pulse output origin search function set to be used.
	•	Normal inputs	Interrupt inputs	Quick-response inputs	High-speed counters	Origin search
А	.0				High-speed counter 0 (phase-A, increment, or count input) fixed	
В	0				High-speed counter 0 (phase-B, decrement, or direction input) fixed	
Z	0				High-speed counter 0 (phase-Z/reset) fixed	Pulse 0: Origin input signal (line driver)
А	.1				High-speed counter 1 (phase-A, increment, or count input) fixed	
В	1				High-speed counter 1 (phase-B, decrement, or direction input) fixed	
Z	1				High-speed counter 1 (phase-Z/reset) fixed	Pulse 1: Origin input signal (line driver)
CIO 0	Bit 00	Normal input 0	Interrupt 0	Quick-response input 0		Pulse 2: Origin proximity input signal
	Bit 01	Normal input 1	Interrupt 1	Quick-response input 1	High-speed counter 2 (phase-Z/reset)	
	Bit 04	Normal input 2			High-speed counter 2 (phase-A, increment, or count input)	
	Bit 05	Normal input 3			High-speed counter 2 (phase-B, decrement, or direction input)	
	Bit 10	Normal input 4			High-speed counter 3 (phase-A, increment, or count input)	
	Bit 11	Normal input 5			High-speed counter 2 (phase-B, decrement, or direction input)	Pulse 3: Origin proximity input signal
CIO 1	Bit 00	Normal input 6	Interrupt 2	Quick-response input 2	High-speed counter 2 (phase-Z/reset)	Pulse 3: Origin input signal
	Bit 01	Normal input 7	Interrupt 3	Quick-response input 3		Pulse 2: Origin input signal
	Bit 02	Normal input 8	Interrupt 4	Quick-response input 4		Pulse 1: Origin input signal (open collector)
	Bit 03	Normal input 9	Interrupt 5	Quick-response input 5		Pulse 0: Origin input signal (open collector)
	Bit 04	Normal input 10				Pulse 1: Origin proximity input signal
	Bit 05	Normal input 11				Pulse 0: Origin proximity input signal

These areas are for line-driver inputs, so they can be used only for high-speed counters (1 MHz) and not for other purposes, such as normal inputs.

■ Built-in Output Area

● CP1H-Y CPU Units

Instr	uctions	When the instructions to the right are not executed	•	output instruction , or ORG) is executed	When the origin search function is set to be used in the PLC Setup, and an origin search is executed by the ORG instruction	When the PWM instruction is executed
DI C	Setup	Normal output		Fixed duty ratio p	pulse output	Variable duty ratio pulse output
PLC	Setup	Normai output	CW/CCW	Pulse plus direction	When the origin search function is used	PWM output
C	WO	Not supported.	Pulse output 0 (CW) fixed	Pulse output 0 (pulse) fixed		
CC	:wo	Not supported.	Pulse output 0 (CCW) fixed	Pulse output 1 (pulse) fixed		
C	W1	Not supported.	Pulse output 1 (CW) fixed	Pulse output 0 (direction) fixed		
CC	W1	Not supported.	Pulse output 1 (CCW) fixed	Pulse output 1 (direction) fixed		
CIO	Bit 04	100.04	Pulse output 2 (CW)	Pulse output 2 (pulse)		
100	Bit 05	100.05	Pulse output 2 (CCW)	Pulse output 2 (direction)		
	Bit 06	100.06	Pulse output 3 (CW)	Pulse output 3 (pulse)		
	Bit 07	100.07	Pulse output 3 (CCW)	Pulse output 3 (direction)		
CIO	Bit 00	101.00			Origin search 2 (Error counter reset output)	PWM output 0
101	Bit 01	101.01			Origin search 3 (Error counter reset output)	PWM output 1
	Bit 02	101.02			Origin search 0 (Error counter reset output)	
	Bit 03	101.03			Origin search 1 (Error counter reset output)	

These areas are for line-driver inputs, so they can be used only for high-speed counters (1 MHz) and not for other purposes, such as normal inputs.

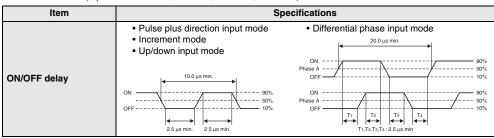
I/O Specifications for CPU Units

■ Input Specifications

		Specifications	
ITEM	High-speed counter inputs (phases A and B)	Interrupt inputs and quick-response inputs	Normal inputs
CP1H-XA/X CPU Units	CIO 0.04 to CIO 0.11	CIO 0.00 to CIO 0.03 and CIO 1.00 to CIO 1.03	CIO 1.04 to CIO 1.11
CP1H-Y CPU Units	CIO 0.04, CIO 0.05, CIO 0.10, CIO 0.11	CIO 0.00, CIO 0.01 and CIO 1.00 to CIO 1.03	CIO 1.04, CIO 1.05
Input voltage	24 VDC +10%/-15%		
Applicable sensors	2-wire sensors or 3-wire sensors		
Input impedance	3.0 kΩ		4.7 kΩ
Input current	7.5 mA typical		5 mA typical
ON voltage	17.0 VDC min.		14.4 VDC min.
OFF voltage/current	1 mA max. at 5.0 VDC		
ON delay	2.5 μs max.	50 μs max.	1 ms max.
OFF delay	2.5 μs max.	50 μs max.	1 ms max.
Circuit configuration	Input LED Input LED Internal circuits	Input LED Internal circuits	Input LED Internal circuits

● High-speed Counter Function Input Specifications

CP1H-XA/X CPU Units (Input bits: CIO 0.04 to CIO 0.11) CP1H-Y CPU Units (Input bits: CIO 0.04, CIO 0.05, CIO 0.10, CIO 0.11)



● Interrupt Input Counter Mode

CP1H-XA/X CPU Units (Input bits: CIO 0.00 to CIO 0.03, CIO 1.00 to CIO 1.03) CP1H-Y CPU Units (Input bits: CIO 0.00, CIO 0.11, CIO 1.00 to CIO 1.03)

Item	Specifications
ON/OFF delay	ON

High-speed Counter Inputs (Line-driver Inputs)

CP1H-Y CPU Units

Item	Specifications				
High-speed counter in- puts	Phases A and B Phase Z				
Input voltage	RS-422A line-driver, AM26LS31 or equivalent Note: The power supply voltage on the line-driver must be 5 V±5% max.				
Input type	Line-driver input				
Input current	10 mA typical	13 mA typical			
Circuit configuration	330 Ω 680 Ω \$330 pF	180 Ω 560 Ω 6800 pF Internal circuits			
ON/OFF delay	 Pulse plus direction input mode Increment mode Up/down input mode Up/down input mode 1 μs min. 0.5 μs min 0.5 μs min 0.5 μs min 0.7 μsas B 0 0FF OFF OFF	ON Phase Z OFF			

■ Output Specifications

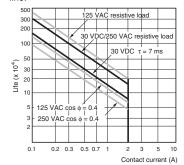
● CPU Units with Relay Outputs

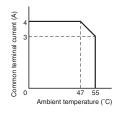
	Item		Specifications		
Max. s	Max. switching capacity		2 A, 250 VAC (cosφ = 1), 2 A, 24 VDC 4 A/common)		
Min. sv	witching	capacity	5 VDC, 10 mA		
Ser-	Elec-	Resis- tive load	100,000 operations (24 VDC)		
vice life of relay	trical	Induc- tive load	48,000 operations (250 VAC, cosφ = 0.4)		
	Mecha	nical	20,000,000 operations		
ON del	lay		15 ms max.		
OFF de	elay		15 ms max.		
Circuit	Circuit configuration		Output LED OUT		

Note: Under the worst conditions, the service life of output contacts is as showr

on the left.

The service life of relays is as shown in the following diagram as a guideline.



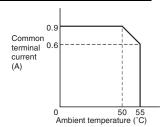


CPU Units with Transistor Outputs (Sinking/Sourcing)

Item	Specifications				
CP1H-XA/X CPU Units	CIO 100.00 to CIO 100.07	CIO 101.00, CIO 101.01	CIO 101.02 to CIO 101.07		
CP1H-Y CPU Units	CIO 100.04 to CIO 100.07	CIO 101.00, CIO 101.01	CIO 101.02, CIO 101.03		
Max. switching capacity	4.5 to 30 VDC: 300 mA/point, 0.9 A/common, 3.6 A/Unit *1*2				
Min. switching capacity	4.5 to 30 VDC, 1 mA				
Leakage current	0.1 mA max.				
Residual voltage	0.6 V max.	1.5 V max.			
ON delay	0.1 ms max.				
OFF delay	0.1 ms max.	0.1 ms max. 1 ms max.			
Fuse	1/common *3	1/common *3			
Circuit configuration	Sinking Outputs OUT	Sinking Outputs Sourcing Outputs Internal circuits Sourcing Outputs	COM (+) COM (+) 4.5 to 30 VDC		

Note: 1. Do not apply a voltage or connect a load to an output terminal exceeding the maximum switching capacity.

- Also do not exceed 0.9 A for the total for CIO 100.00 to CIO 100.03. (CIO 100.00 to CIO 100.03 is different common.)
- *2 *3 A maximum of 0.9 A per common can be switched at an ambient temperature of 50 $^{\circ}\text{C}.$
- Fuses cannot be replaced by the user.



Pulse outputs

CP1H-XA/X CPU Units: Output bits CIO 100.00 to CIO 100.07 CP1H-Y CPU Units: Output bits CIO100.04 to CIO 100.07

Item	Specifications
Max. switching capacity	30 mA at 4.75 to 26.4 VDC
Min. switching capacity	7 mA at 4.75 to 26.4 VDC
Max. output frequency	100 kHz
Output waveform	OFF 90% ON 10% 4 ms min. 2 ms min.

Note: 1. The above values assume a resistive load and do not consider the impedance of the cable connecting the load.

- The pulse widths during actual use may be smaller than the ones shown above due to pulse distortion caused by connecting cable impedance.
- 3. The OFF and ON refer to the output transistor. The output transistor is ON at level "!"

Pulse Outputs (Line-driver Outputs)

CP1H-Y CPU Units

Item	Specifications		
Pulse outputs	Line-driver outputs, Am26LS31 or equivalent		
Max. output current	20 mA		
Max. output frequency	1 MHz		
Circuit configuration	cwu- ccmu+ ccmu+		

Note: Connect a load of 20 mA or less to the output. The Unit may be damaged if a current of more than 20 mA is output.

Pulse outputs

CP1H-XA/X/Y CPU Units: Output bits CIO101.00, CIO 101.01

Item	Specifications
Max. switching capacity	30 mA at 4.75 to 26.4 VDC
Max. output frequency	1 kHz
PWM output precision	ON duty +5%, -0% at output frequency of 1 kHz
Output waveform	OFF ON $\frac{1}{T}$ ON $\frac{1}{T}$ ON $\frac{1}{T}$ $\frac{1}{T}$ $\frac{1}{T}$ $\frac{1}{T}$ $\frac{1}{T}$ $\frac{1}{T}$

Note: 1. The above values assume a resistive load and do not consider the impedance of the cable connecting the load.

- The pulse widths during actual use may be smaller than the ones shown above due to pulse distortion caused by connecting cable impedance.
- The OFF and ON refer to the output transistor. The output transistor is ON at level "L".

■ Analog I/O Specifications (CP1H-XA CPU Units Only)

	Item	Voltage I/O	Current I/O			
	Number of analog inputs	ļ				
	Input signal range	0 to 5 V, 1 to 5 V, 0 to 10 V, or –10 to 10 V	0 to 20 mA or 4 to 20 mA			
	Max. rated input	±15 V	±30 mA			
	External input impedance	1 M Ω min.	Approx. 250 Ω			
Analog Input	Resolution	1/6,000 or 1/12,000 (full scale)				
Section	Overall accuracy	25°C: ±0.3% full scale/0 to 55°C: ±0.6% full scale	25°C: ±0.4% full scale/0 to 55°C: ±0.8% full scale			
	A/D conversion data	ull scale for -10 to 10 V: F448 (E890) to 0BB8 (1770) hex ull scale for other ranges: 0000 to 1770 (2EE0) hex				
	Averaging	Supported (Set for individual inputs in the PLC Setup.)				
	Open-circuit detection	Supported (Value when disconnected: 8000 Hex)				
	Number of outputs	2				
	Output signal range	0 to 5 V, 1 to 5 V, 0 to 10 V, -10 to 10 V	0 to 20 mA or 4 to 20 mA			
Analog	Allowable external output load resistance	1 kΩ min.	600 Ω max.			
Output	External output impedance	0.5Ω max.				
Section	Resolution	1/6000 or 1/12000 (full scale)				
	Overall accuracy	25°C±0.4% of full scale, 0 to 55°C±0.8% of full scale				
	D/A conversion data	Full scale for –10 to 10 V: F448 (E890) to 0BB8 (1770) hex Full scale for other ranges: 0000 to 1770 (2EE0) hex				
Conversion time		1 ms/point				
Isolation	method	Photocoupler isolation between analog I/O terminals and internal circuits. No isolation between analog I/O signals.				

Built-in Analog Input Switch (Factory Settings)

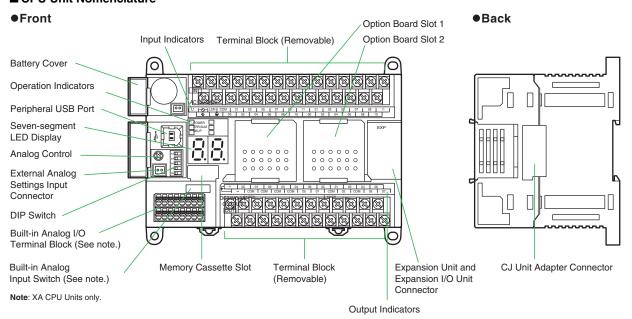


Built-in Analog I/O Terminal Block Arrangement

AD1+	AD1-	AD2+	AD2-	AD3+	AD3-	AD4+	AD4-
0	0	0	0	0	0	0	0
VOUT	IOUT1	COM1	VOUT2	IOUT2	COM2	AG	AG
\Box	0	\overline{C}	0	0	0	0	0

External Interfaces

■ CPU Unit Nomenclature



Option Unit Specifications

■ Serial Communications Specifications (CP1W-CIF01/-CIF11)

Item	Function	Interface
Peripheral USB port	For connecting Peripheral Device.	Conforms to USB 1.1, B-type connector
Serial port 1 (Option board slot 1)	Host Link, No-protocol, NT Link (1: N), Serial PLC Link (See note.),	The following can be used for either port. CP1W-CIF01 RS-232C Option Board CP1W-CIF11
Serial port 2 (Option board slot 2)	Serial Gateway (CompoWay/F master, Modbus-RTU master), Modbus-RTU easy master function, ToolBus	RS-422A/485 Option Board (Maximum transmission distance 50m) CP1W-CIF12 RS-422A/485(Isolated-type) Option Board (Maximum transmission distance 500m) Can be used with either port.

Note: Serial PLC Link can be used with either serial port 1 or serial port 2.

■ Ethernet Communications Specifications (CP1W-CIF41)

Item			Specifications
Applicable	PLCs		CP1H CPU Units
Number of	Number of Units that can be mounted		2 sets. (The CP1W-CIF41 Ver.1.0 and Ver.2.0 can be combined and used with one CPU Unit. When using CP1W-CIF41 Ver.1.0, only one unit can be mounted in an option board slot.)
	Media access method		CSMA/CD
	Modulation method		Baseband
	Transmission paths		Star form
	Baud rate		100 Mbit/s (100Base-TX), 10 Mbit/s (10Base-T)
Transfer	Transmission media 100 Mbit/s 10 Mbit/s	100 Mbit/s	• Unshielded twisted-pair (UDP) cable Categories: 5, 5e • Shielded twisted-pair (STP) cable Categories: $100~\Omega$ at 5, 5e
		10 Mbit/s	• Unshielded twisted-pair (UDP) cable Categories: 3, 4, 5, 5e • Shielded twisted-pair (STP) cable Categories: $100~\Omega$ at 3, 4, 5, 5e
	Transmission Distance		100 m (distance between hub and node)

Item		FINS Communications Service Specifications
Number of node	es	254
Message length	1	1016 bytes max.
Size of buffer		8k
Communication	ns Function	FINS Communications Service (UDP/IP, TCP/IP)
	Protocol used	UDP/IP
FINS/UDP	Server/Client	Only server (Cannot be used as a client)
method	Port number	9600 (default) Can be changed.
	Protection	No
	Protocol used	TCP/IP
FINS/TCP	Server/Client	Only server (Cannot be used as a client)
method	Number of connections	Up to 2 simultaneous connections and only one connection can be set to client
	Port number	9600 (default) Can be changed.
	Protection	Yes (Specification of client IP addresses when unit is used as a server)

- Note: 1. CX-Programmer version 8.1 or higher (CX-One version 3.1 or higher) is required.

 2. Use CX-Integrator version 2.33 or higher (CX-One version 3.1 or higher) when the system needs to be set the routing tables. However, CX-Integrator does not support the other functions, using CP1W-CIF41, such as transferring the parameters and network structure.

 3. To connect the CP1H CPUs with the NS-series Programmable Terminals via Ethernet using CP1W-CIF41, make sure that the system version of NS Series is 0.0 or bit leave.
 - is 8.2 or higher.

■ LDC Option Board (CP1W-DAM01) • Specifications

Item	Function
Mounting port	CP1H: Option board slot 1
Communications protocol	Peripheral bus (Turn ON DIP switch pin 4.)
Number of display characters	4 rows × 12 characters: 48 characters max.
Display characters	5 × 7 dots (alphanumeric and symbols).
Backlight	Electroluminescence (EL): Normal: Lit green; Error: Flashing red

● LCD Functions

Operation		Description			
Changing operating modes		Change the PLC operating mode without using the CX-Programmer.			
I/O memory		Read and change the present values in the memory areas and force-set or force-reset bits.			
PLC Setup of	perations	Read and change the PLC Setup.			
Analog I/O monitor		Monitor the analog adjustment and present va	alue for the external analog setting input.		
Error log display		Read the log of errors that have occurred.			
Memory cassette operation		Transfer and verify user programs between the	ne PLC and memory cassette.		
User monitor settings		Read the status of up to 16 words and bits wi	th comments. You can use this setting to read data on the startup display.		
Message display function settings		Display a user-set message of up to 48 chara A maximum of 16 screens can be registered	acters on the LCD Option Board when a specified bit turns ON. for display.		
			Operation:		
	Day timer	Use this timer for ON/OFF switching at a specified times every day from the starting day of the week to the ending day of the week. Sixteen timers cam be set from timer 01 to timer 16.	Starting day of the week Example: Monday ON OFF Starting time Ending time Starting time Example: 9:00 Example: 17:00 9:00 17:00 Ending day of the week Example: Friday Starting time Ending time Ending time Ending time Example: 9:00 Example: 17:00		
Timers	Weekly timer	Use this timer for ON/OFF operation in intervals of one week that starts one day and ends another day. Sixteen timers cam be set from timer No. 01 to timer No. 16.	Operation: Starting day of the week Example: Friday ON OFF Starting time Example: 12:00 Ending time Example: 8:00 Starting time Example: 8:00 Starting time Example: 8:00 Ending time Example: 8:00		
	Calendar timer	Use the calendar timers for ON or OFF operation in intervals of one year from the starting day to the ending day. Sixteen timers can be set from timer 01 to timer 16.	Operation: ON OFF Starting day July 1 August 31 Set September 1 August 31 as the ending day.		
Saving setting		Save the various settings that you set with the LCD Option Board to the DM Area of the PLC. You can also write the settings saved in the PLC to the LCD Option Board.			
Language		Changing the display language (Japanese/English)			
Other functions		Setting the time of the PLC's built-in clock Reading system data (e.g., unit version and Setting the backlight lighting time Adjusting LCD contrast Reading cycle time (e.g., average, maximur Clearing data for the LCD Option Board			

Expansion I/O Unit Specifications

■ CP1W-40EDR/40EDT/40EDT1/32ER/32ET/32ET1/20EDR1/20EDT1/16ER/16ET/16ET/16ET/8ED/8ER/8ET/8ET1 Expansion I/O Units

Expansion I/O Units can be connected to the CPU Unit to configure the required number of I/O points.



● DC Inputs (CP1W-40EDR/40EDT/40EDT1/20EDR1/20EDT/20EDT1/8ED)

Item	Specifications		
Input voltage	24 VDC +10%/-15%		
Input impedance	4.7 kΩ		
Input current	5 mA typical		
ON voltage	14.4 VDC min.		
OFF voltage 5.0 VDC max.			
ON delay	0 to 32 ms max. (Default: 8 ms) (See note 1.)		
OFF delay	0 to 32 ms max. (Default: 8 ms) (See note 1.)		
Circuit configuration	Input LED Internal circuits		

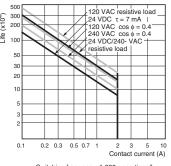
Note: 1. Do not apply a voltage exceeding the rated voltage to an input terminal.

2. Can be set in the PLC Setup to 0, 0.5, 1, 2, 4, 8, 16 or 32 ms. The CP1W-40EDR/EDT/EDT1 are fixed at 16 ms.

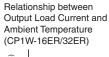
● Relay Outputs (CP1W-40EDR/32ER/20EDR1/16ER/8ER)

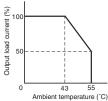
	Item		Specifications	
Max. switching capacity		apacity	2 A, 250 VAC (cosφ = 1), 24 VDC 4 A/common	
Min. swit	ching c	apacity	5 VDC, 10 mA	
Service Elec- load			150,000 operations (24 VDC)	
life of relay	trical	Inductive load	100,000 operations (24 VAC cos = 0.4)	
Mechanical		nical	20,000,000 operations	
ON delay	,		15 ms max.	
OFF dela	ıy		15 ms max.	
OFF delay Circuit configuration		ation	Output LED OUT	

Note: Under the worst conditions, the service life of output contacts is as shown on the left. The service life of relays is as shown in the following diagram as a guideline.



Switching frequency: 1,800 operations/h





When using the CP1W-32ER, do not allow more than 24 outputs to be ON simultaneously regardless of the ambient temperature.

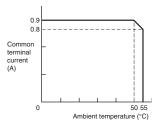
Transistor Outputs (Sinking/Sourcing) (CP1W-40EDT/-40EDT1/-32ET/-32ET1/-20EDT/-20EDT1/-16ET/-16ET1/-8ET/-8ET1)

		Specifications				
Item	CP1W-40EDT	CP1W-32E	CP1W-20EDT	CP1W-16ET	CP1W-8ET	
	CP1W-40EDT1	CP1W-32ET1	CP1W-20EDT1	CP1W-16ET1	CP1W-8ET1	
Max. switching capacity (See note 3.)	4.5 to 30 VDC: 0.3 A/point		24 VAC +10%/ -5%: 0.3 A/point	4.5 to 30 VDC: 0.3 A/point	OUT00/01 4.5 to 30 VDC, 0.2 A/output OUT02 to 07 4.5 to 30 VDC, 0.3A/output	
	0.9 A/common		0.9 A/common	0.9 A/common	0.9 A/common	
	3.6 A/common		1.8 A/common	3.6 A/common	1.8 A/common	
Leakage current	0. 1mA max.					
Residual voltage	1.5 V max.					
ON delay	0.1ms max.					
OFF delay	1 ms max. at 24 \ +10%/-5%, 5 to 3					
Max number of Simultaneously ON Points of Output	16 pts (100%) 24 pts (75%)		8 pts (100%)	16 pts (100%)	8 pts (100%)	
Fuse (See note 2.)	1/common	1				
Circuit configura- tion	Sinking Outputs Output LED O		OUT 4.5 to			

Note: 1. Do not apply a voltage or connect a load to an output terminal exceeding the maximum switching capacity.

2. The fuses cannot be replaced by the

- user.
- $\boldsymbol{3.}\,\,\boldsymbol{A}$ maximum of 0.9 A per common can be switched at an ambient temperature of 50°C.

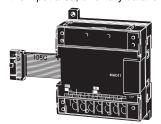


■ CP1W-AD041/DA041/DA021/MAD11 Analog Units

Analog values that are input are converted to binary data and stored in the input area, or binary data is output as analog values.







■ Analog Input Unit: CP1W-AD041

Model		CP1W-AD041		
Item		Input voltage	Input current	
Number of	of inputs	4		
Input sign	nal range	0 to 5 V, 1 to 5 V, 0 to 10 V, -10 to 10 V	0 to 20 mA 4 to 20 mA	
Max. rate	d input	±15 V	±30 mA	
External input impedance		1 MΩ min.	Approx. 250 Ω	
Resolution	n	6000		
Overall	25°C	±0.3% of full scale	±0.4% of full scale	
accura- cy	0 to 55°C	±0.6% of full scale	±0.8% of full scale	
Conversi	on time	2 ms/point (8ms/4points)		
A/D conv	ersion	Binary data with resolution of 6,000 Full scale for –10 to 10 V: F448 to 0BB8 hex Full scale for other ranges: 0000 to 1770 hex		
Averagin	g	Supported.		
Open-circuit detection		Supported.		
Isolation method		Photocoupler isolation between analog I/O and internal circuits (There is no isolation between the analog I/O signals.)		

■ Analog Output Unit: CP1W-DA041/DA021

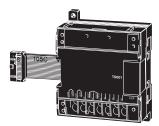
	Model	CP1W-DA	041/DA021	
Item		Input voltage	Input current	
Number of outputs		DA041: 4, DA021: 2		
Output si range	gnal	1 to 5 V, 0 to 10 V, -10 to +10 V	0 to 20 mA or 4 to 20 mA	
Allowable nal outpu resistance	it load	2 kΩ min.	350 Ω max.	
External impedant	•	0.5 Ω max.		
Resolution	n	6000		
Overall	25°C	±0.4% of full scale		
accura- cy	0 to 55°C	±0.8% of full scale		
Conversi	on time	2 ms/point (8ms/4points, 4ms/2points)		
D/A conversion data		Binary data with resolution of 6,000 Full scale for –10 to 10 V: F448 to 0BB8 hex Full scale for other ranges: 0000 to 1770 hex		
Isolation method		Photocoupler isolation between analog I/O and internal circuits (There is no isolation between the analog I/O signals.)		

■ Analog I/O Unit: CP1W-MAD11

	Model		CP1W-MAD11		
Item	Item		Voltage I/O	Current I/O	
	Number o f inputs		2 inputs		
	Input signal range		0 to 5 V, 1 to 5V, 0 to 10 V, or -10 to 10V	0 to 20 mA, 4 to 20 mA	
	Max. rated inp	ut	±15 V	±30 mA	
	External input	impedance	1 M Ω min.	250 Ω	
Analog	Resolution		1/6000 (full scale)		
Input	Overall	25°C	±0.3% of full scale	±0.4% of full scale	
Section	accuracy	0 to 55°C	±0.6% of full scale	±0.8% of full scale	
	A/D conversion data		Binary data (hexadecimal, 4 digits) -10 to 10 V: F448 to 0BB8 hex Full scale for other ranges: 0000 to 1770 hex		
	Averaging		Supported (Set for each input using a DIP switch.)		
	Disconnection	detection	Supported		
	Number of outputs		1 output		
	Output signal range		1 to 5 V, 0 to 10 V, -10 to 10 V	0 to 20 mA, 4 to 20 mA	
	External output max. current				
Analan	Allowable external output load resistance		1 k Ω min.	600 $Ω$ max.	
Analog Output	External input impedance		$0.5~\Omega$ max.		
Section	Resolution		1/6000 (full scale)		
	Overall	25°C	±0.4% of full scale		
	accuracy	0 to 55°C	±0.8% of full scale		
	D/A conversion data		Binary data (hexadecimal, 4 digits) -10 to 10 V: F448 to 0BB8 hex Full scale for other ranges: 0000 to 1770 hex		
Conversion	n time		2 ms/point (6 ms for all points)		
Isolation method			Photocoupler isolation between analog I/O and internal circuits (There is no isolation between the analog I/O signals.)		

■ Temperature Sensor Units: CP1W-TS001/TS002/TS101/TS102

By mounting a Temperature Sensor Unit to the PLC, inputs can be obtained from thermocouples or platinum resistance thermometers, and temperature measurements can be converted to binary data (4-digit hexadecimal) and stored in the input area of the CPU Unit.



Specifications

Item Mod	CP1W-TS001/002	CP1W-TS101/102	
Number of inputs	2 (TS001), 4 (TS002)	2 (TS101), 4 (TS102)	
Input types	K, J switchable (Note: Same for all inputs.)	Pt100, JPt100 switchable (Note: Same for all inputs.)	
Indication accuracy	(The larger of the indicated value: ±0.5% and ±2°C (See note.)) ±1 (The larger of the indicated value: ±0.5% and ±1°C) ±1 digit max.		
Conversion time	250 ms/2 points (TS001, TS101); 250 ms/4 points (TS002, TS102)		
Converted tempera- ture data	Binary (4-digit hexadecimal)		
Isolation method	Photocoupler isolation between the temperature input signals.		

Note: The indication accuracy when using a K-type thermocouple for temperature less than -100°C is ± 4 °C ± 1 digit max.

Input Temperature Ranges for CP1W-TS001/002 (The rotary switch can be used to make the following range and input type settings.)

Input type	Range (°C)	Range (°F)
K	-200 to 1300	-300 to 2300
K	0.0 to 500.0	0.0 to 900.0
	-100 to 850	-100 to 1500
J	0.0 to 400.0	0.0 to 750.0

Input Temperature Ranges for CP1W-TS101/102 (The rotary switch can be used to make the following range and input type settings.)

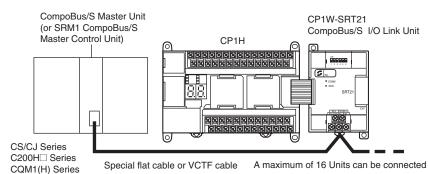
Input type	Range (°C)	Range (°F)
Pt100	-200.0 to 650.0	-300 to 1200.0
JPt100	-200.0 to 650.0	-300 to 1200.0

■ CP1W-SRT21 CompoBus/S I/O Link Unit

The CompoBus/S I/O Link Unit functions as a slave for a CompoBus/S Master Unit (or an SRM1 CompoBus/S Master Control Unit) to form an I/O Link with 8 inputs and 8 outputs between the CompoBus/S I/O Link Unit and the Master Unit.



SRM1 Series CPM2C-S Series



Specifications

Item Model	CP1W-SRT21
Master/Slave	CompoBus/S Slave
Number of I/O bits	8 input bits, 8 output bits
Number of words occupied in CP1H I/O memory	1 input word, 1 output word (Allocated in the same way as for other Expansion Units)
Node number setting	Set using the DIP switch (before the CPU Unit is turned ON.)

I/O Bits and I/O Allocations

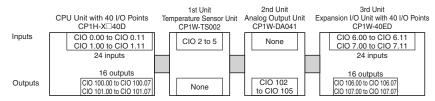
With CP1H CPU Units, the beginning input and output words (CIO 0 and CIO 100) are allocated by the CPU Unit one or two words at a time. I/O bits are allocated in word units in order of connection to Expansion Units and Expansion I/O Units connected to a CPU Unit.

CPU Unit	Allocated words		
or o onit	Inputs	Outputs	
CP1H CPU Unit with 40 I/O points	CIO 0 and CIO 1	CIO 100 and CIO 101	

Note: For details on the number of words allocated to Expansion Units and Expansion I/O Units, refer to Words Allocated to CP1W Expansion Units and Expansion I/O Units on page 23.

● Example: I/O Bit Allocations When Expansion Units Are Connected

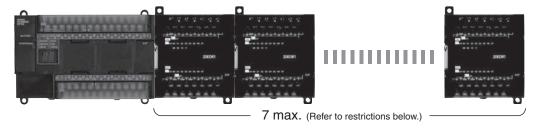
CPU Unit with 40 I/O Points + Temperature Sensor Unit + Analog Output Unit + Expansion I/O Unit with 40 I/O Points



Expansion Unit and Expansion I/O Units Specifications

■ Maximum Number of CP1W/CPM1A Expansion Unit and Expansion I/O Units

CP1H CPU Unit



■ Restrictions on the Number of CP1H Expansion Unit and I/O Unit Connections

Up to seven Expansion Units and Expansion I/O Units can be connected when a CP1H CPU Unit is used, but the following restrictions apply. Observe these restrictions when using the models in the shaded areas in the following tables. A maximum total of 15 input words is allocated for Expansion Units and a maximum total of 15 output words is allocated for Expansion Units and Expansion I/O Units.

● Words Allocated to CP1W Expansion Units and Expansion I/O Units

			No. of	words
	Unit type	Model	Input	Output
		CP1W-40EDR		
	40 I/O points	CP1W-40EDT	2	2
		CP1W-40EDT1		
		CP1W-32ER		
	32 outputs	CP1W-32ET		4
		CP1W-32ET1		
		CP1W-20EDR1		
Expansion	20 I/O points	CP1W-20EDT	1	1
I/O Units		CP1W-20EDT1		
		CP1W-16ER		
	16 outputs	CP1W-16ET		2
		CP1W-16ET1		
	8 inputs	CP1W-8ED	1	
	8 outputs	CP1W-8ER		
		CP1W-8ET		1
		CP1W-8ET1		
	2 analog inputs, 1 analog output	CP1W-MAD11	2	1
Analog Units	4 analog inputs	CP1W-AD041	4	2
	4 analog outputs	CP1W-DA041		4
	2 analog outputs	CP1W-DA021		2
	2 thermocouple inputs	CP1W-TS001	2	
	4 thermocouple inputs	CP1W-TS002	4	
Temperature Sensor Units	2 platinum resistance thermometer inputs	CP1W-TS101	2	
	4 platinum resistance thermometer inputs	CP1W-TS102	4	
CompoBus/S I/O Link Unit	8 inputs and 8 outputs	CP1W-SRT21	1	1

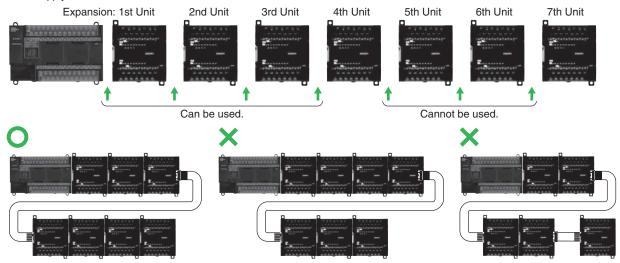
For example, the CP1W-TS002 Temperature Sensor Unit is allocated four words per Unit, so no more than three Units can be connected (4 words x 3 Units = 12 words). It would then be possible to mount a combination of other Units to use the remaining three input and 15 output words.

Examples of Possible Combinations

Number of Units	Input	Output
CP1H-X40DR-A		
CP1W-TS002 x 3	4 words x 3 Units = 12 words	0 words
CP1W -TS001 x 1	2 words x 1 Unit = 2 words	0 words
CP1W -20EDR1 x 1	1 word x 1 Unit = 1 word	1 word x 1 Unit = 1 word
CP1W - DA041 x 2	0 words	4 words x 2 Units = 8 words
Total: 7 Units	Total: 15 words	Total: 9 words
≤7 Units	≤15 words	≤15 words

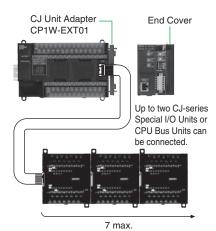
■ Using CP1W-CN811 I/O Connecting Cable

- I/O Connecting Cable can be connected to any Unit from the CP1H CPU Unit to the third Expansion Unit or Expansion I/O Unit (i.e., the fourth Unit).
- Only one I/O Connecting Cable can be used in each CP1H PLC.
- Even when I/O Connecting Cable is used, the above restrictions on the number of connectable CP1W/CPM1A Expansion Units and Expansion I/O Units still apply.



■ Using CJ-series Special I/O Units or CPU Bus Units with a CP1H CPU Unit

Up to two CJ-series Special I/O Units or CPU Bus Units can be connected by using a CP1W-EXT01 CJ Unit Adapter. The number of Units that can be used is as described below.



Use CP1W-CN811 I/O Connecting Cable when using CP1W Expansion Units and Expansion I/O Units at the same time as a CJ Unit Adapter. In this situation, the number of CP1W Expansion Unit and Expansion I/O Units that can be connected is subject to the restrictions described above. Only one I/O Connecting Cable can be used.

● CJ-series Special I/O Units and CPU Bus Units (For details, refer to the CJ1 Catalog (Cat. No. P052)).

Unit name	Model	5 V Current consumption (A)	
	CJ1W-AD042	0.52 A	
Analog Input Units	CJ1W-AD081-V1	0.42 A	
	CJ1W-AD041-V1	0.42 A	
	CJ1W-DA042V	0.40 A	
	CJ1W-DA08V	0.14 A	
Analog Output Units	CJ1W-DA08C	0.14 A	
	CJ1W-DA041	0.12 A	
	CJ1W-DA021	U. 1∠ A	
Analog I/O Unit	CJ1W-MAD42	0.58 A	
	CJ1W-PH41U	0.30 A	
	CJ1W-AD04U	0.32 A	
Donate de la della de	CJ1W-PTS51	0.25 A	
Process Input Units	CJ1W-PTS52		
	CJ1W-PTS15		
	CJ1W-PDC15	0.18 A	
	CJ1W-TC001		
	CJ1W-TC002		
	CJ1W-TC003		
Temperature Control	CJ1W-TC004	0.05.4	
Units	CJ1W-TC101	0.25 A	
	CJ1W-TC102	Ţ	
	CJ1W-TC103	İ	
	CJ1W-TC104	İ	
CompoBus/S Master Unit	CJ1W-SRM21	0.15 A	
CompoNet Master Unit	CJ1W-CRM21	0.40 A	

Unit name	Model	5 V Current consumption (A)
	CJ1W-NC113	0.05.4
	CJ1W-NC213	0.25 A
Desiries Osstal Heite	CJ1W-NC413	0.36 A
Position Control Units	CJ1W-NC133	0.05.4
	CJ1W-NC233	0.25 A
	CJ1W-NC433	0.36 A
High-speed Counter Unit	CJ1W-CT021	0.25 A
	CJ1W-V680C11	0.26 A (24 VDC 0.13 A)
ID Sensor Units	CJ1W-V680C12	0.32 A (24 VDC 0.26 A)
ID Sensor Office	CJ1W-V600C11	0.26 A (24 VDC 0.12 A)
	CJ1W-V600C12	0.32 A (24 VDC 0.24 A)
	CJ1W-SCU42	0.38 A*
	CJ1W-SCU22	0.29 A*
Serial Communications	CJ1W-SCU32	0.46 A
Units	CJ1W-SCU41-V1	0.38 A*
	CJ1W-SCU21-V1	0.28 A*
	CJ1W-SCU31-V1	0.38 A
Ethernet Unit	CJ1W-ETN21	0.37 A
EtherNet/IP Unit	CJ1W-EIP21	0.41 A
DeviceNet Unit	CJ1W-DRM21	0.33 A
Controller Link Unit	CJ1W-CLK23	0.35 A
	CJ1W-NC271	
MECHATROLINK-II	CJ1W-NC471	0.36 A
Position Control Unit	CJ1W-NCF71	U.30 A
	CJ1W-NCF71-MA	
FL-net Unit	CJ1W-FLN22	0.37 A
High-speed Data Storage Unit	CJ1W-SPU01-V2	0.56 A

er when NT-AL001 Link Adapters are used.

Current Consumption

Based on the current consumption when CJ-series Special I/O Units or CPU Bus Units are used with a CP1H CPU Unit, the
maximum number of Units that can be used is two CJ-series Units and seven CP1W Expansion Units and Expansion I/O
Units.

The current consumption for the CP1H must be no more than 2 A for 5 V and 1 A for 24 V, and the total current consumption must be no more than 30 W.

Check the total current consumption to be sure these limits are not exceeded referring to page 25 for the CP1H CPU Unit and CP1W Expansion Unit and Expansion I/O Unit current consumptions and to the above table for CJ-series Unit current consumptions.

CPU Units

Model	Current consumption		External power supply	
Model	5 VDC	24 VDC	24 VDC (See note 5.)	
CP1H-X40DR-A	0.42 A	0.07 A	0.3 A max. (0.9 A max.)	
CP1H-X40DT-D	0.50 A	0.01 A		
CP1H-X40DT1-D	0.50 A	0.02 A		
CP1H-XA40DR-A	0.43 A	0.18 A	0.3 A max. (0.8 A max.)	
CP1H-XA40DT-D	0.51 A	0.12 A		
CP1H-XA40DT1-D	0.51 A	0.15 A		
CP1H-Y20DT-D	0.55 A			

Note: 1. The current consumption of the CP1W-ME05M Memory Cassette and the CP1W-CIF01/CIF11 Option Boards are included in the current consumption of the CP1U Init

- 2. CPU Units with DC power do not provide an external power supply.
- 3. The current consumptions given in the following table must be added to the current consumption of the CPU Unit if an Expansion Unit or Expansion I/O Unit is connected.
- 4. The external power supply cannot be used if an Expansion Unit or Expansion I/O Unit is connected to a CPU Unit with 14 or 20 I/O points.
- 5. Values in parentheses are the maximum external power supply for a CPU Unit to which an Expansion I/O Unit is not connected. Refer to the CP1H CPU Unit Operation Manual (Cat. No. W450) for details.

Option Units

Unit name	Model	Current c	Current consumption	
Unit name	Model	DC5V	24 VDC	
RS-232C Option Board	CP1W-CIF01	*		
RS-422A/485 Option Board	CP1W-CIF11	*		
RS-422A/485 (Isolated-type) Option Board	CP1W-CIF21	0.075A		
Ethernet Option Board	CP1W-CIF41	0.130A		
LCD Option Board	CP1W-DAM01	0.020A		
Memory Cassette	CP1W-ME05M	*		
CJ Unit Adapter	CP1W-EXT01	*		

^{*} The current consumption of the following is included with the current consumption of the CPU Unit: CP1W-ME05M Memory Cassette, CP1W-CIF-01 or CP1W-CIF11 Option Board, and CP1W-EXT01 CJ Unit Adapter.

• Others : Equipment that uses internal power supply of PLC

Unit	nama	Model	Current co	Current consumption	
Unit name		Wodel	5 VDC	24 VDC	
Link Adapter		CJ1W-CIF11	0.04A		
		NT-AL001	0.15A		
Backlight (Green/Orange/Red)		NV3W-MG20L	0.2A		
NV3W	Backlight (White/Pink/Red)	NV3W-MR20L	0.2A		

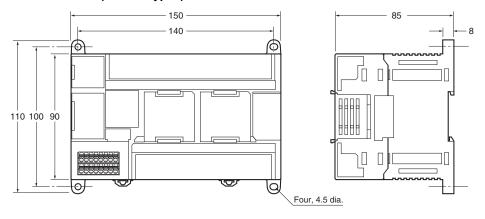
● Expansion Units and Expansion I/O Units

Unit name		Model	Current consumption	
		модеі	5 VDC	24 VDC
40 I/O points		CP1W-40EDR	0.080 A	0.090 A
	24 inputs	CP1W-40EDT		
	16 outputs	CP1W-40EDT1	0.160 A	
		CP1W-32ER	0.049 A	0.131 A
	32 outputs	CP1W-32ET	0.440.4	
		CP1W-32ET1	0.113 A	
	20 I/O points	CP1W-20EDR1	0.103 A	0.044 A
Evnancian I/O Unita	12 inputs	CP1W-20EDT	0.400.4	
Expansion I/O Units	8 outputs	CP1W-20EDT1	0.130 A	
	16 outputs	CP1W-16ER	0.042 A	0.090 A
		CP1W-16ET	0.070.4	
		CP1W-16ET1	0.076 A	
	8 inputs	CP1W-8ED	0.018 A	
		CP1W-8ER	0.026 A	0.044 A
	8 outputs	CP1W-8ET	0.075 A	
		CP1W-8ET1		
Analog Input Unit	4 inputs	CP1W-AD041	0.100 A	0.090 A
Analog Output Unit	4 outputs	CP1W-DA041	0.080 A	0.124 A
Analog Output Offic	2 outputs	CP1W-DA021	0.040 A	0.095 A
Analog I/O Unit	2 inputs and 1 output	CP1W-MAD11	0.083 A	0.110 A
	K or J thermocouple	CP1W-TS001	0.040.4	0.0=0.1
	inputs	CP1W-TS002	0.040 A	0.059 A
Temperature Sensor Units	Pt or JPt platinum	CP1W-TS101		
	resistance thermometer inputs	CP1W-TS102	0.054 A	0.073 A
CompoBus/S I/O Link Unit	8 inputs and 8 outputs	CP1W-SRT21	0.029 A	

Dimensions (Unit: mm)

■ CPU Units

CP1H CPU Units (X/XA/Y Types)

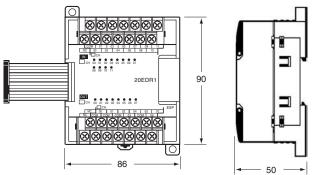


■ Expansion Units and Expansion I/O Units

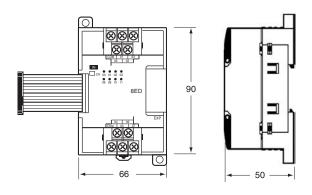
CP1W-20ED□ CP1W-16E□□

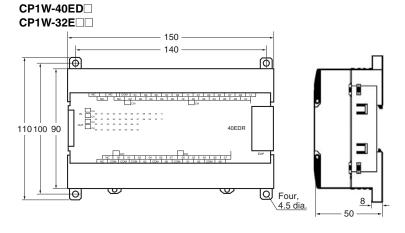
CP1W-AD041/CP1W-DA041/CP1W-DA021

 $\mathbf{CP1W\text{-}MAD11/CP1W\text{-}TS} \square \square \square$



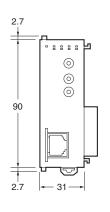
CP1W-8E□□ CP1W-SRT21



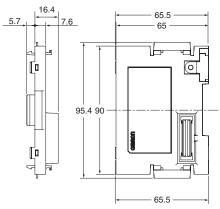


Unit name	Model number	Weight
	CP1W-40ER	380 g
	CP1W-40EDT/-40EDT1	320 g
	CP1W-32ER	465 g
Evnancian I/O	CP1W-32ET/-32ET1	325 g
Expansion I/O Units	CP1W-20EDR1/-20EDT/-20EDT1	300 g
Omis	CP1W-16ER	280 g
	CP1W-16ET/-16ET1	225 g
	CP1W-8ED	200 g
	CP1W-8ER/-8ET/-8ET1	250 g
Analog Units	CP1W-AD041/-DA041/-DA021	200 g
Analog Units	CP1W-MAD11	150 g
Temperature Sensor Units	CP1W-TS001/-TS002/-TS101/ -TS102	250 g
CompoBus/S I/O Link Unit	CP1W-SRT21	200 g

■ CJ-series Special I/O Units and CPU Bus Units



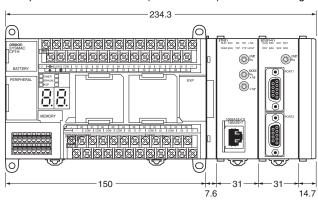
■ CJ Unit Adaptor CP1W-EXT01



Note: It takes an example about the size.

■ CP1H

Example: Two CJ-series Units (31-mm widths) Connected Using a CJ Unit Adapter



Related Manuals

Cat. No.	Model numbers	Manual name	Description
W450	CP1H-X40D□-□ CP1H-XA40D□-□ CP1H-Y20DT-D	CP Series CP1H CPU Unit Operation Manual	Provides the following information on the CP Series:
W451	CP1H-X40D□-□ CP1H-XA40D□-□ CP1H-Y20DT-D	CP Series CP1H CPU Unit Programming Manual	Provides the following information on the CP Series: • Programming instructions • Programming methods • Tasks • File memory • Functions Use this manual together with the CP1H Programmable Controllers Operation Manual (W450).
W342	CS1G/H-CPU H CS1G/H-CPU	CS/CJseries Communications Commands Reference Manual	Describes commands addressed to CS-series and CJ-series CPU Units, including C-mode commands and FINS commands. Note: This manual describes on commands address to CPU Units regardless of the communications path. (CPU Unit serial ports, Serial Communications Unit/Board ports, and Communications Unit ports can be used.) Refer to the relevant operation manuals for information on commands addresses to Special I/O Units and CPU Bus Units.

MEMO

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OMRON Corporation Industrial Automation Company

Tokyo, JAPAN

Contact: www.ia.omron.com

Regional Headquarters
OMRON EUROPE B.V.
Wegalaan 67-69-2132 JD Hoofddorp
The Netherlands Tel: (31)2356-81-300/Fax: (31)2356-81-388

OMRON ASIA PACIFIC PTE. LTD.

No. 438A Alexandra Road # 05-05/08 (Lobby 2), Alexandra Technopark, Singapore 119967 Tel: (65) 6835-3011/Fax: (65) 6835-2711

OMRON ELECTRONICS LLC

One Commerce Drive Schaumburg, IL 60173-5302 U.S.A. Tel: (1) 847-843-7900/Fax: (1) 847-843-7787

OMRON (CHINA) CO., LTD.

Room 2211, Bank of China Tower, 200 Yin Cheng Zhong Road, PuDong New Area, Shanghai, 200120, China Tel: (86) 21-5037-2222/Fax: (86) 21-5037-2200

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